How would you design a register (D-flipflop) using logic gates?

How would you design a series of registers to a 4-bit memory?

Where do you use the register (on-chip or off-chip)? What is its speed for storing or retrieving data (how many clock cycle)?
How would you design a Static RAM (SRAM) using transistors?

How would you construct a series (16-word x 4-bit) of SRAM in an organisation?

Where do you use SRAM (on-chip or off-chip)? What is its speed for storing or retrieving data? What is the usual size of a SRAM?
How would you design a Dynamic RAM (DRAM) using transistor and capacitor?

Can you show a Dynamic RAM schematic with 4 by 4 row and column address selection?

Where do you use the DRAM (on-chip or off-chip)? What is its speed for storing or retrieving data? What is the usual size of a DRAM?
How would you design a ROM with 3 inputs (a, b, c) and 3 outputs (f, g, h) using inverter, AND gates and OR gates?

Where do you use the ROM (on-chip or off-chip)? What is its speed for storing or retrieving data?

What is the usual size of a ROM?
Can you draw the design of a Flash memory? (it is similar to a design of a transistor)

Is Flash memory a ROM or a RAM? And how fast (speed) does it retrieve or store data?

Comparing to a normal ROM, what are the advantages of using a Flash memory?